

# INFORMATION DISCLOSURE STATEMENT

Applicant : Joseph Jeddeloh  
 App. No. : 10/776,439  
 Filed : February 10, 2004  
 For : ACCELERATED GRAPHICS PORT FOR  
 A MULTIPLE MEMORY CONTROLLER  
 COMPUTER SYSTEM  
 Examiner : Unknown  
 Group Art Unit : Unknown

Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450


Dear Sir:

Enclosed is form PTO-1449 listing 96 references. References numbered 1 through 68 are of record in U.S. patent application No. 09/892,917, filed June 26, 2001, which is relied upon for an earlier filing date under 35 U.S.C. § 120. Accordingly, copies of references numbered 1 through 68 are not submitted pursuant to 37 C.F.R. § 1.98(d).

This Information Disclosure Statement is being filed with an RCE or within three months of the filing date of this application and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1), (b)(2), or (b)(4).

Respectfully submitted,  
 KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 5/4/2004

By:   
 Eric M. Nelson  
 Registration No. 43,829  
 Attorney of Record  
 Customer No. 20,995  
 (619) 235-8550

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. MTIPAT.007C1C1	APPLICATION NO. 10/776,439
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT Jeddeloh	
(USE SEVERAL SHEETS IF NECESSARY)		FILING DATE February 10, 2004	GROUP Unknown

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1	4,016,545	04/05/77	Lipovski			
	2	4,507,730	03/26/85	Johnson, et al.			
	3	4,937,734	06/26/90	Bechtolsheim			
	4	4,969,122	11/06/90	Jenson			
	5	5,109,520	04/1992	Knierim	345	509	
	6	5,121,487	06/09/92	Bechtolsheim			
	7	5,133,058	07/21/92	Jenson			
	8	5,155,816	10/13/92	Kohn			
	9	5,222,222	06/22/93	Mehring			
	10	5,263,142	11/16/93	Watkins, et al.			
	11	5,265,213	11/23/93	Weiser, et al.			
	12	5,265,227	11/23/93	Kohn, et al.			
	13	5,265,236	11/23/93	Mehring, et al.			
	14	5,305,444	04/19/94	Becker, et al.			
	15	5,313,577	05/17/94	Meinerth, et a.			
	16	5,315,696	05/24/94	Case, et al.			
	17	5,315,698	05/24/94	Case, et al.			
	18	5,321,806	06/14/94	Meinerth, et al.			
	19	5,321,807	06/14/94	Mumford			
	20	5,321,810	06/14/94	Case, et al.			
	21	5,321,836	06/14/94	Crawford, et al.			
	22	5,361,340	11/01/94	Kelly, et al.			
	23	5,392,393	02/21/95	Deering			
	24	5,396,614	03/07/95	Khalidi, et al.			
	25	5,408,605	04/18/95	Deering			
	26	5,426,750	06/20/95	Becker, et al.			
	27	5,440,682	08/08/95	Deering			

EXAMINER

DATE CONSIDERED

\*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

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## U.S. PATENT DOCUMENTS

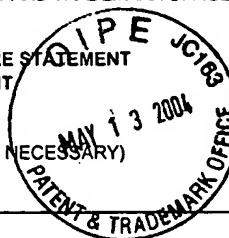
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	28	5,442,755	08/1995	Shibata			
	29	5,446,854	08/29/95	Khalidi, et al.			
	30	5,465,337	11/07/95	Kong			
	31	5,479,627	12/26/95	Khalidi, et al.	395	415	
	32	5,491,806	02/13/96	Horstmann, et al.			
	33	5,500,948	03/19/96	Hinton, et al.			
	34	5,524,233	06/04/96	Milburn, et al.			
	35	5,542,062	07/30/96	Taylor, et al.			
	36	5,546,555	08/13/96	Horstmann, et al.			
	37	5,548,739	08/20/96	Yung			
	38	5,553,023	09/03/96	Lau, et al.			
	39	5,584,014	12/10/96	Nayfeh, et al.			
	40	5,586,283	12/17/96	Lopez-Aguado, et al.			
	41	5,664,161	09/02/97	Fukushima, et al.	345	501	
	42	5,664,162	09/02/97	Dye			11/03/94
	43	5,675,750	10/07/97	Matsumoto, et al.	395	309	
	44	5,675,762	10/07/97	Bodin, et al.			01/06/95
	45	5,737,765	04/07/98	Shigeeda	711	170	
	46	5,778,197	07/07/98	Dunham	395	284	
	47	5,815,167	09/29/98	Muthal, et al.	345	512	
	48	5,845,327	12/01/98	Rickard, et al.	711	146	
	49	5,854,637	12/29/98	Sturgess	345	512	
	50	5,854,638	12/1998	Tung	345	512	
	51	5,861,893	01/19/99	Sturgess	345	525	
	52	5,889,970	03/30/99	Horan, et al.	710	126	
	53	5,892,964	04/06/99	Horan, et al.	712	33	
	54	5,909,559	06/01/99	So	710	127	
	55	5,911,051	06/08/99	Carson, et al.	710	107	

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		FILING DATE February 10, 2004	GROUP Unknown



## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	56	5,911,149	06/1999	Luan et al.			
	57	5,914,727	06/22/99	Horan, et al.	345	503	
	58	5,999,183	12/07/99	Kilgariff, et al.	345	418	
	59	6,002,411	12/1999	Dye	345	521	
	60	6,008,823	12/1999	Rhoden et al.			
	61	6,118,462	09/12/00	Magulis	345	521	
	62	6,230,223	5/2001	Olariq			
	63	6,233,702	05/2001	Horst et al.			

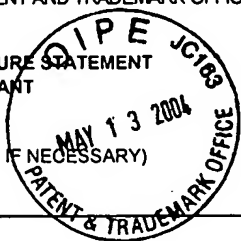
## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	64	WO92/02879	02/20/92	PCT				
	65	WO97/26604	07/24/97	PCT				

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
	66	Accelerated Graphics Port Interface Specification, Revision 1.0, Intel Corporation, July 31, 1996.
	67	LSI Logic L64852 Mbus-to-Sbus Controller (M2S) Technical Manual. LSI Logic Corporation, 1993.
	68	Intel Advance Information "INTEL 440LX AGPSET: 822443LX PCI A.G.P. Controller (PAC)," August 1997, 118pp.

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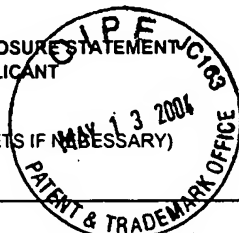
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	69	4,429,363	01/1984	Duke et al.			
	70	5,386,547	01/1995	Jouppi			
	71	5,568,620	10/1996	Sarangdhar et al.			
	72	5,579,493	11/1996	Kiuchi et al.			
	73	5,594,882	01/1997	Bell			
	74	5,615,343	03/1997	Sarangdhar et al.			
	75	5,617,554	04/01/97	Alpert et al.			
	76	5,623,627	04/1997	Witt			
	77	5,630,094	05/1997	Hayek et al.			
	78	5,632,021	05/1997	Jennings et al.			
	79	5,682,516	10/1997	Sarangdhar et al.			
	80	5,721,864	02/1998	Chiarot et al.			
	81	5,724,549	03/1998	Selgas et al.			
	82	5,761,444	06/1998	Ajanovic et al.			
	83	5,761,462	06/1998	Neal et al.			
	84	5,774,685	06/1998	Dubey			
	85	5,778,425	07/1998	Shigeeda			
	86	5,802,603	09/1998	Bains et al.			
	87	5,809,522	09/1998	Novak et al.			
	88	5,829,038	10/1998	Merrell et al.			
	89	5,867,180	02/1999	Katayama et al.			
	90	5,870,567	02/1999	Hausauer et al.			
	91	5,881,253	03/1999	Seeman			
	92	5,930,827	07/1999	Sturges			
	93	5,937,171	08/1999	Sarangdhar et al.			

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FOREIGN PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	94	0 710 913 A1	10/1995	Europe				
	95	0 803 807 A2	10/1997	Europe				

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)							
	96	Intel, <i>Intel 450KX/GX PCIset Overview</i> , pp. 1-172 (1996).						

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PATENT

Case Docket No. MTIPAT.007C1C1

Date: May 4, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Joseph Jeddeloh  
Appl. No. : 10/776,439  
Filed : February 10, 2004  
For : ACCELERATED GRAPHICS  
PORT FOR A MULTIPLE  
MEMORY CONTROLLER  
COMPUTER SYSTEM  
Examiner : Unknown  
Group Art Unit : Unknown

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

May 4, 2004

  
(Date)

Eric M. Nelson, Reg. No. 43,829

TRANSMITTAL LETTER

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) An Information Disclosure Statement.
- (X) A PTO Form 1449 listing ninety-six (96) references, enclosing twenty-eight (28) references.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.
- (X) Return prepaid postcard.



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